

FIG. 1

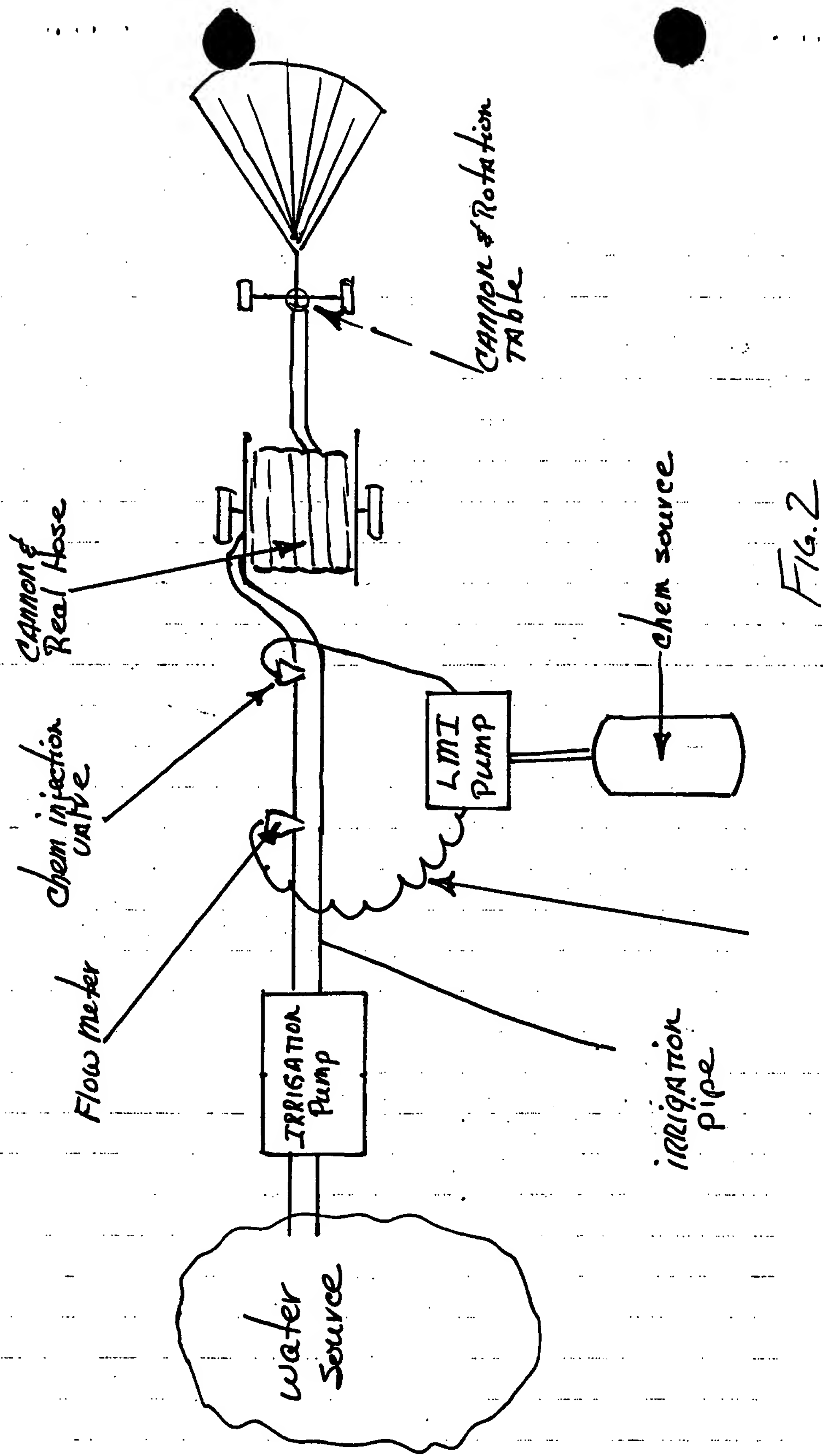
[illegible]

FIG. 3 is a schematic diagram of a system for

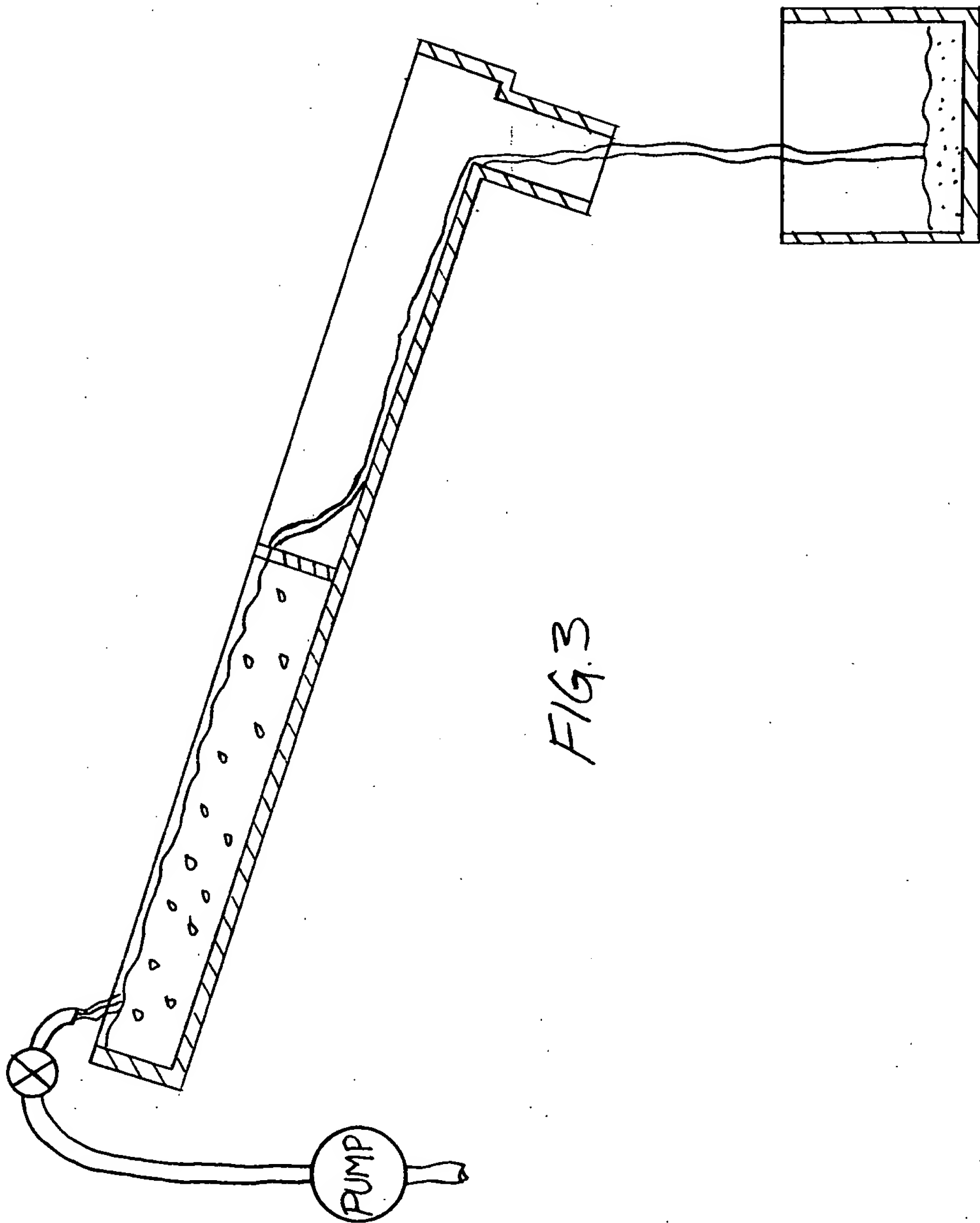


FIG. 3